

Clean Version

2. The image display device as defined in claim 1, whercin:
only one of the parts and entireties of the drive circuit(s) operates at any given time.
3. The image display device as defined in claim 1, whercin:
the same part(s) and entirety(ies) of the drive circuit(s) is(are) driven throughout one or more frame periods.
4. The image display device as defined in claim 1, wherein:
two or more of the parts and entireties of the drivc circuit(s) are switchably driven in one frame period.
5. The image display device as defind in claim 1, wherein:
at least two of the parts and entireties of the drivc circuit(s) write image data in respective areas on a screen.
6. The image display device as defined in claim 1, wherein:
a part or entirety of the data signal line drive circuit is provided in plurality; and
at least two of the parts and entireties of the data signal line drive circuit write image data in one partial or whole area on a screen in one frame period.
11. The image display device as defined in claim 1, whercin:
a part or entirety of the data signal line drive circuit is provided in plurality; and
at least onc of the parts and entircties of the data signal linc drive circuit writes image data in a blanking period of each horizontal scan period.
12. The image display device as defined in claim 1, wherein:
a part or entirety of the data signal line drive circuit is provided in plurality; and
at least one of the parts and entireties of the data signal line drive circuit writes image data with a predetermined delay from another part or entirety of the data signal line drive circuit.

13. The image display device as defined in claim 1, wherein:
the parts and entireties of the drive circuit(s) are located opposing one another across the
pixel array.

126. The image display device as defined in claim 122, wherein:
only one of the parts and entireties of the drive circuit(s) operates at any given time.

127. The image display device as defined in claim 122, wherein:
the same part(s) and entirety(ies) of the drive circuit(s) is(are) driven throughout one or
more frame periods.

128. The image display device as defined in claim 122, wherein:
two or more of the parts and entireties of the drive circuit(s) are switchably driven in one
frame period.

129. The image display device as defined in claim 122, wherein:
at least two of the parts and entireties of the drive circuit(s) write image data in respective
areas on a screen.

130. The image display device as defined in claim 122, wherein:
a part or entirety of the data signal line drive circuit is provided in plurality; and
at least two of the parts and entireties of the data signal line drive circuit write image data
in one partial or whole area on a screen in one frame period.

131. The image display device as defined in claim 122, wherein:
a part or entirety of the data signal line drive circuit is provided in plurality; and
at least one of the parts and entireties of the data signal line drive circuit writes image
data in a blanking period of each horizontal scan period.

132. The image display device as defined in claim 122, wherein:
a part or entirety of the data signal line drive circuit is provided in plurality; and

at least one of the parts and entireties of the data signal line drive circuit writes image data with a predetermined delay from another part or entirety of the data signal line drive circuit.

133. The image display device as defined in claim 122, wherein:
the parts and entireties of the drive circuit(s) are located opposing one another across the pixel array.